

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY: TIRUCHIRAPPALLI – 620 015

Date: 26.12.2012

Short-listed candidates for Ph.D. programme (Part-Time - External / On-campus) in the Department of Electrical and Electronics Engineering, NIT, Tiruchirappalli are directed to appear for the Entrance Test per the details given below:

Venue : IT Building, NIT-Tiruchirappalli

Date & Time : 09.01.2013 & 10.30 a.m. – 11.30a.m.

Written Test : Objective questions

(Each correct answer carries 1Mark & 0.25 Mark less for wrong answer)

Note: 1. Candidates should bring all original and Xerox Copy of the necessary certificates and proof of the papers published, if any.

2. EEE Department has no seat for full-time Ph.D. programme in this session.

The short-listed candidates for Ph.D. programme (Part-Time -External / On-campus) in EEE, NIT-T:

Sl.No.	Appl.No.	Name of the candidate
1.	Ph.D./EE/31	P. JOHN BRITTO
2.	Ph.D/EE/33	W. VINIL DANI
3.	Ph.D/EE/37	R. PURUSOTHAMAN
4.	Ph.D/EE/46	B. MURUGANANTHAM
5.	Ph.D/EE/50	M. MAYAPANDI
6.	Ph.D/EE/60	A. DHEEPANCHAKKRAVARTHY
7.	Ph.D/EE/61	M. GOPINATH MURALI
8.	Ph.D/EE/78	BOYI VENKATA RAMAN
9.	Ph.D/EE/83	MARIMUTHU.M
10.	Ph.D/EE/104	GOLLA SURI BABU
11.	Ph.D/EE/107	P. SUGANTHI
12.	Ph.D/EE/111	PRAVEEN RAJ R.S.

Sd/-----
HOD/EEE